



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815**

Serial No.: **10/050,171**

Examiner: **WARREN, Matthew E.**

Filed: **January 18, 2002**

**P.T.O. Confirmation No.: 7623**

For. **SEMICONDUCTOR DEVICE CAPABLE OF SUPPRESSING CURRENT  
CONCENTRATION IN PAD AND ITS MANUFACTURE METHOD**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: June 8, 2007

Sir:

This is a Preliminary Amendment for the above-captioned patent application. Please amend  
the above-captioned patent application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.